

Figure 1A - 1C

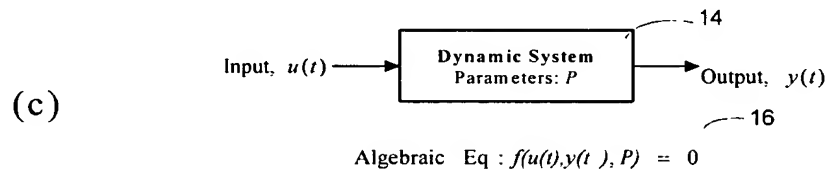
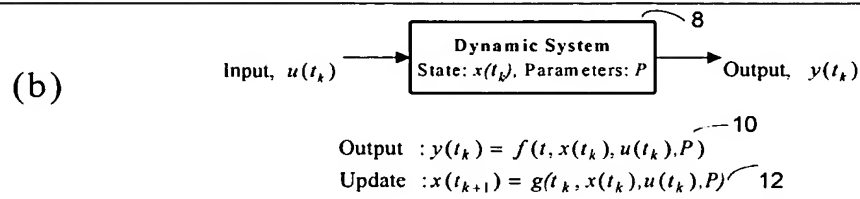
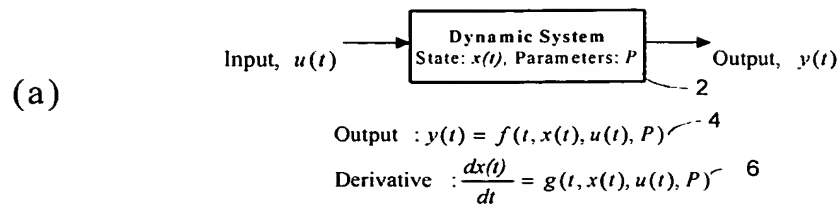


Figure 2

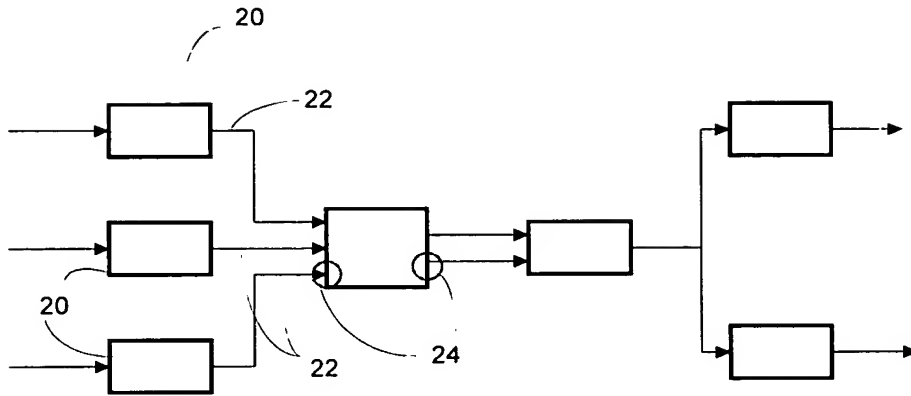


Figure 3

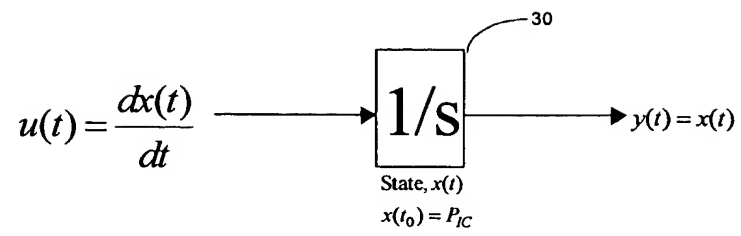


Figure 4

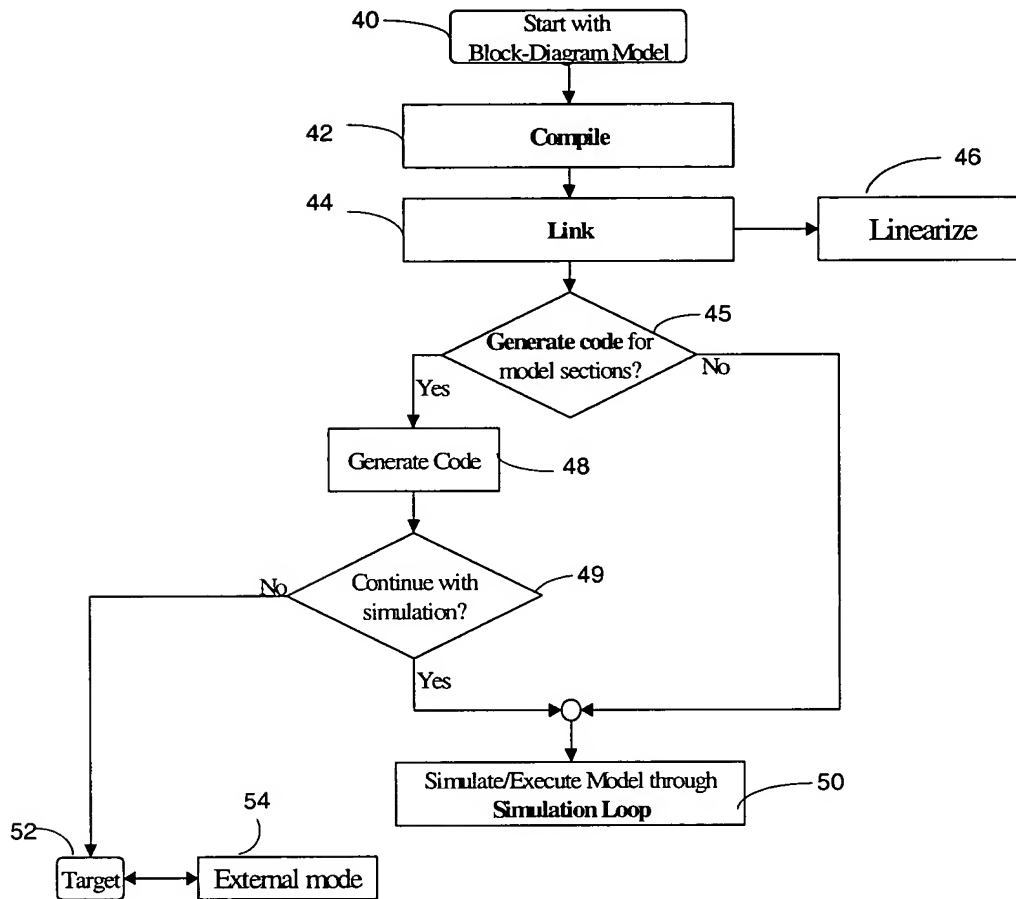
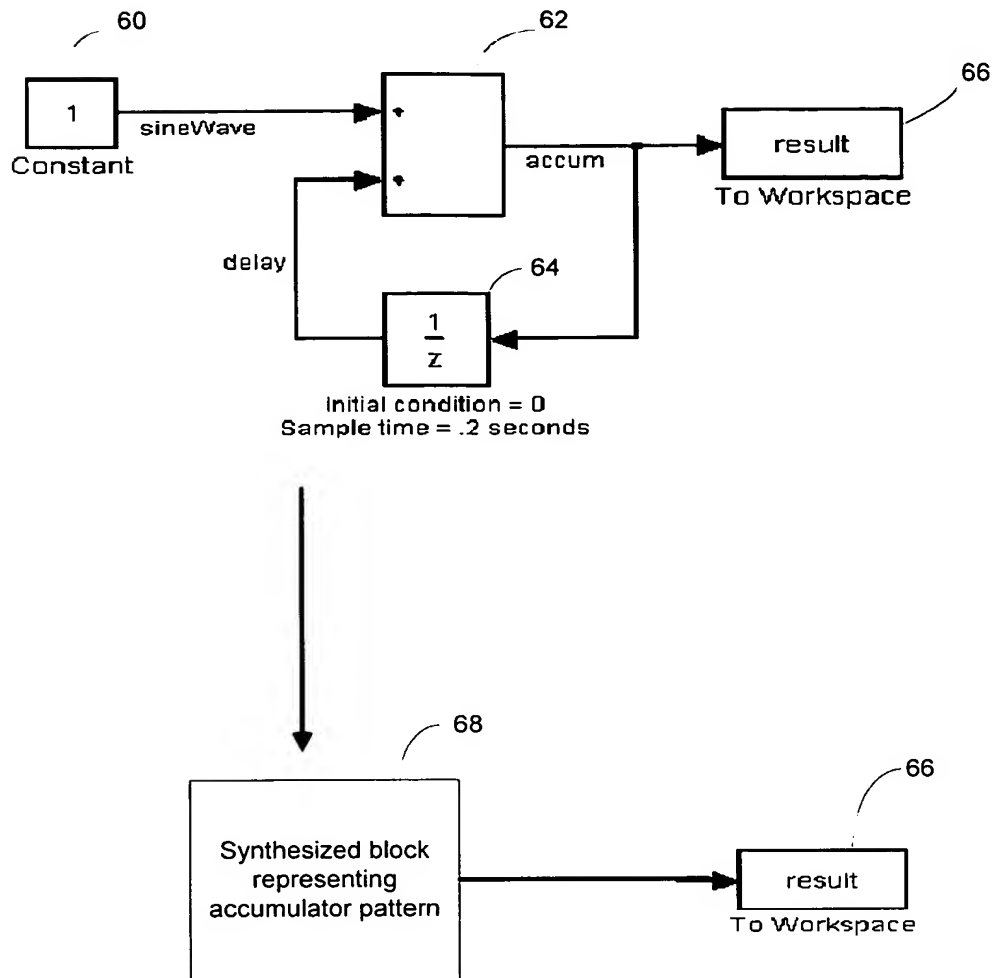
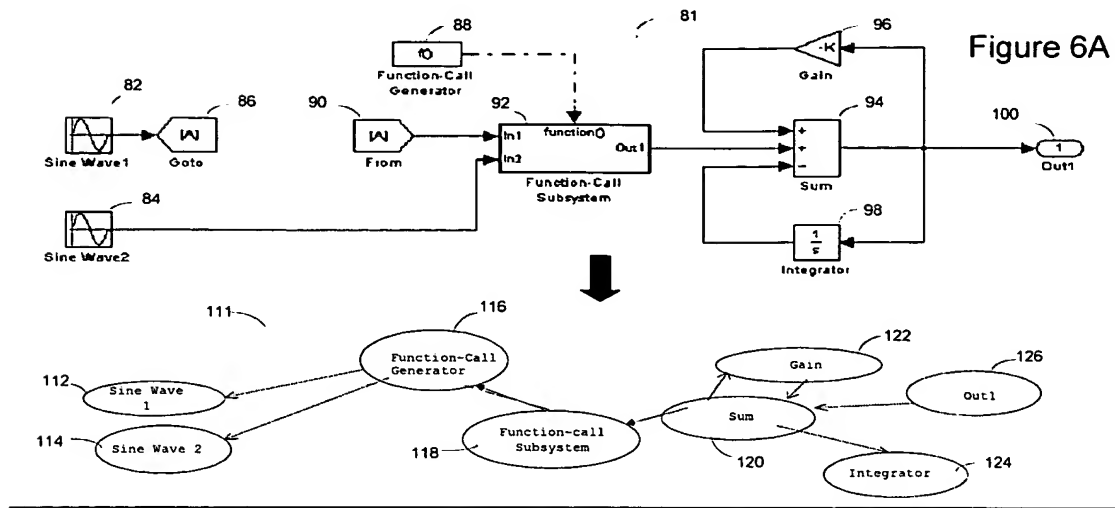


Figure 5





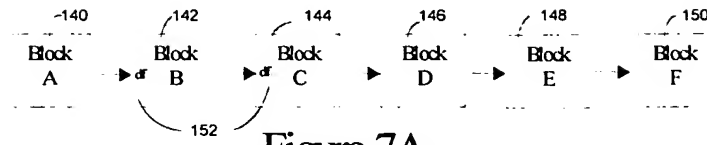


Figure 7A

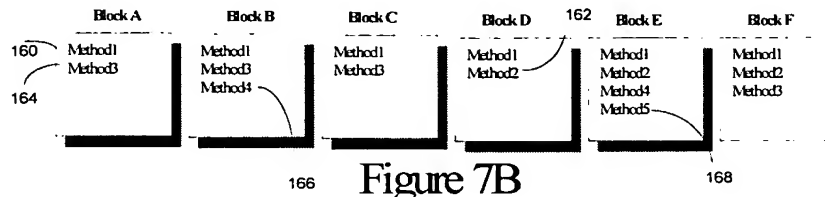


Figure 7B

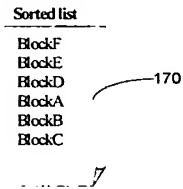


Figure 7C

Figure 8

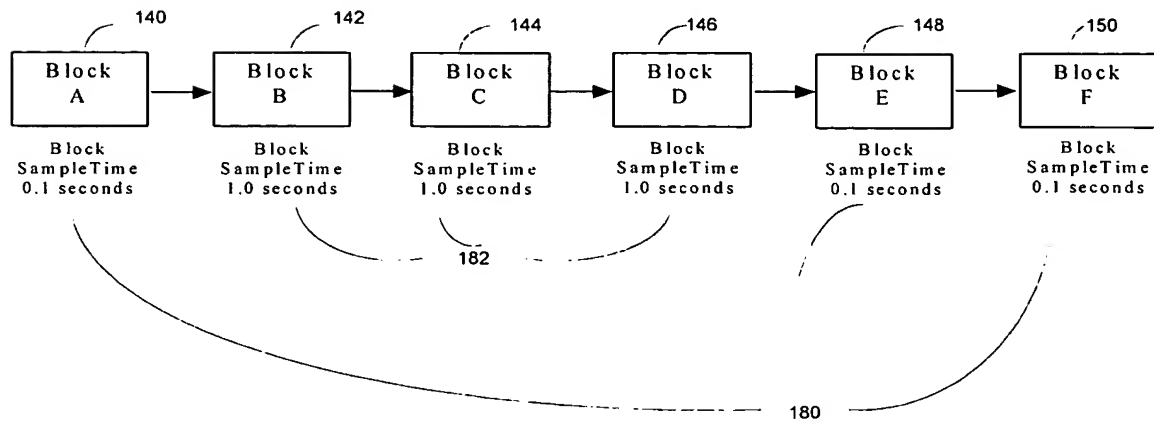


Figure 9

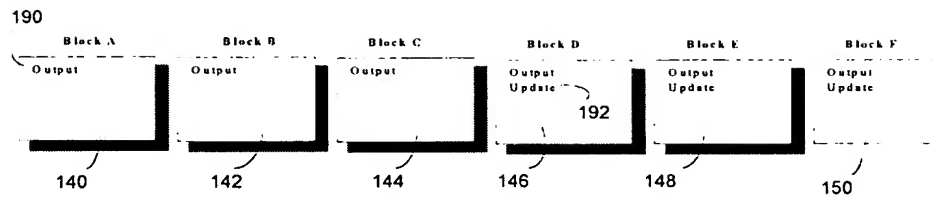


Figure 10

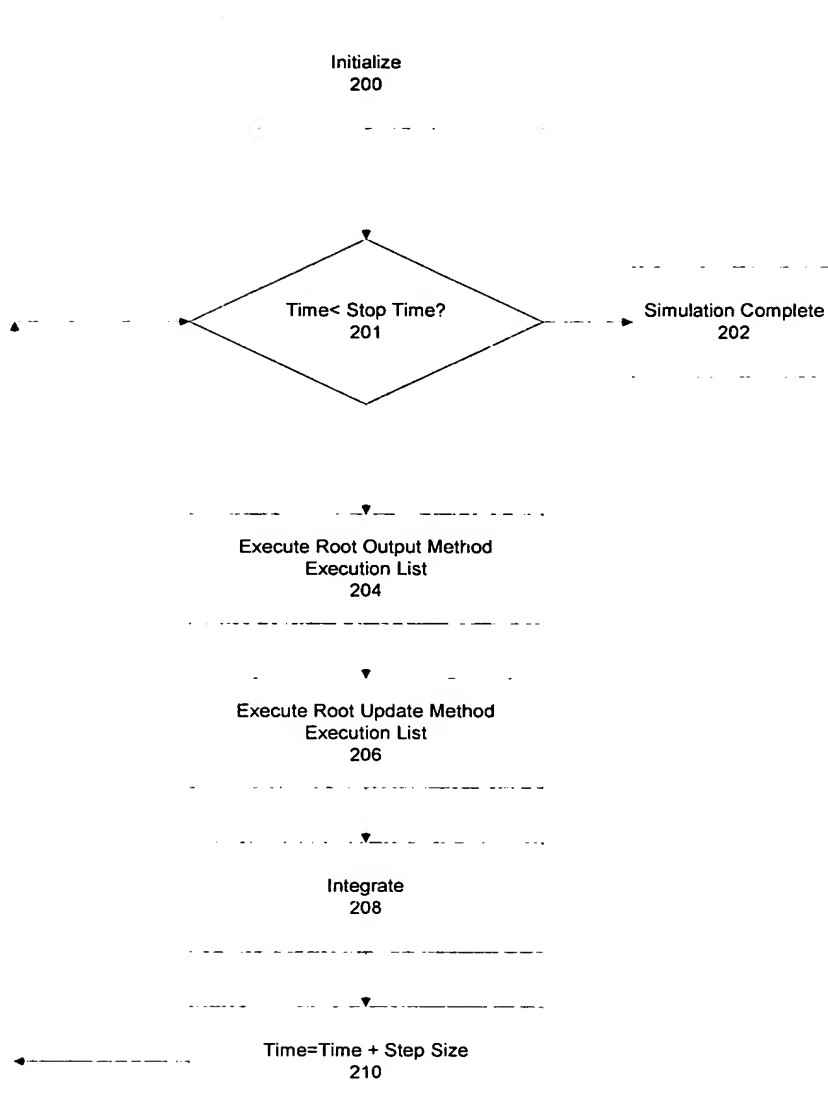
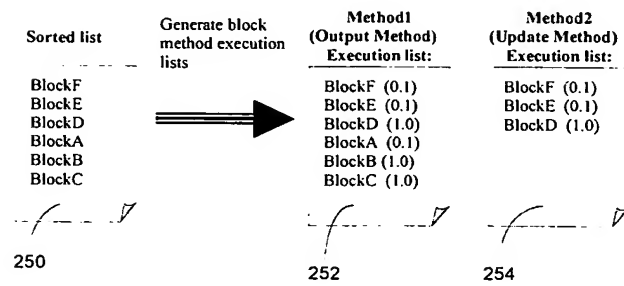


Figure 11A



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Sorted list	Sample time
F (output/update)	0.1
E (output/update)	0.1
D (output/update)	1
A (output)	0.1
B (output)	1
C (output)	1

Figure 11B

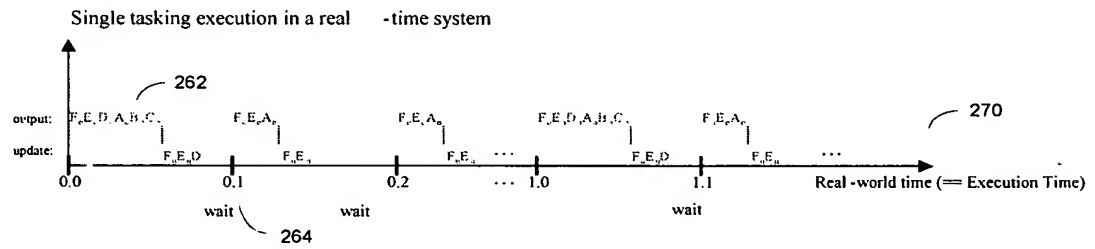
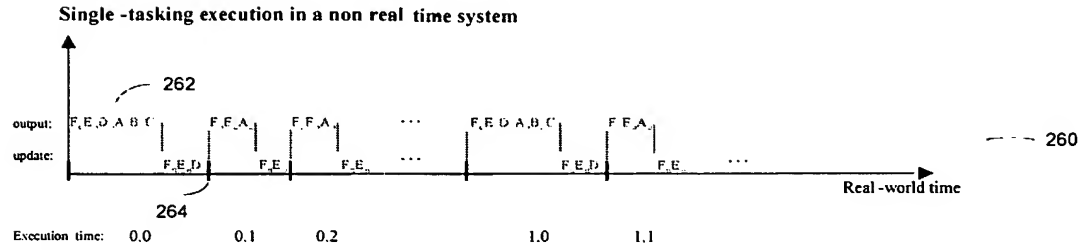


FIGURE 12A

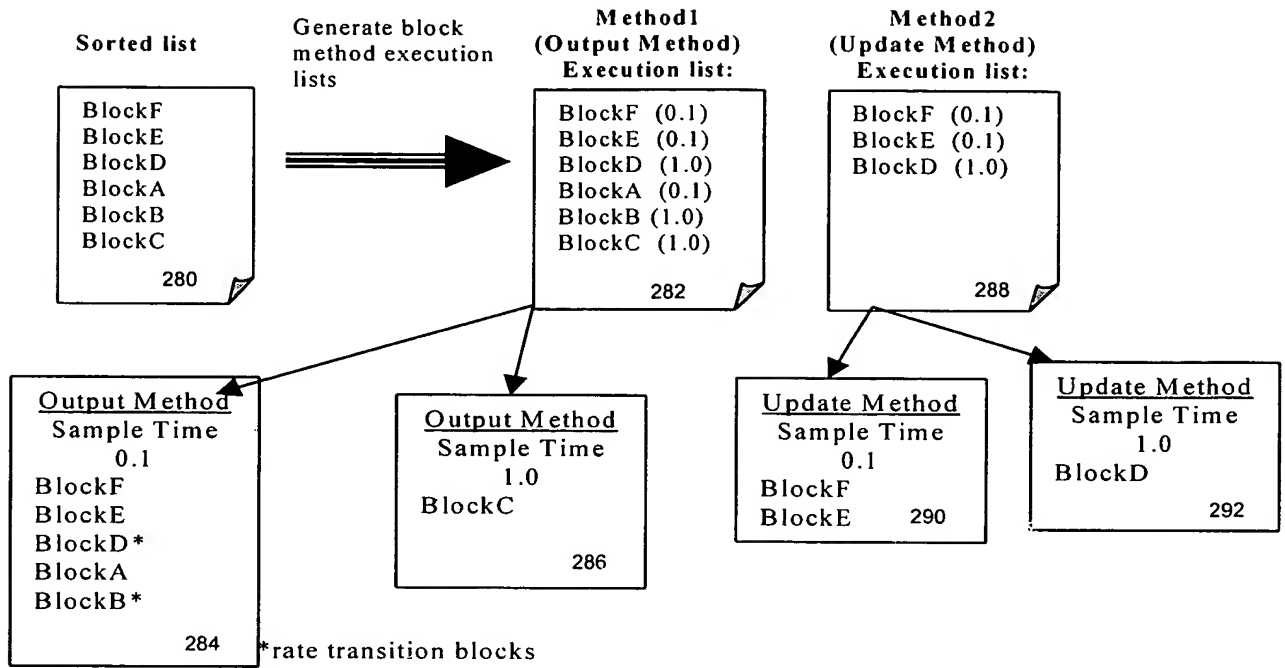


Figure 12B

Sorted list	Sample time
F (output/update)	0.1
E (output/update)	0.1
D (output/update)	1 promoted to 0.1 task
A (output)	0.1
B (output)	1 promoted to 0.1 task
C (output)	1

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Execution in multi-tasking mode

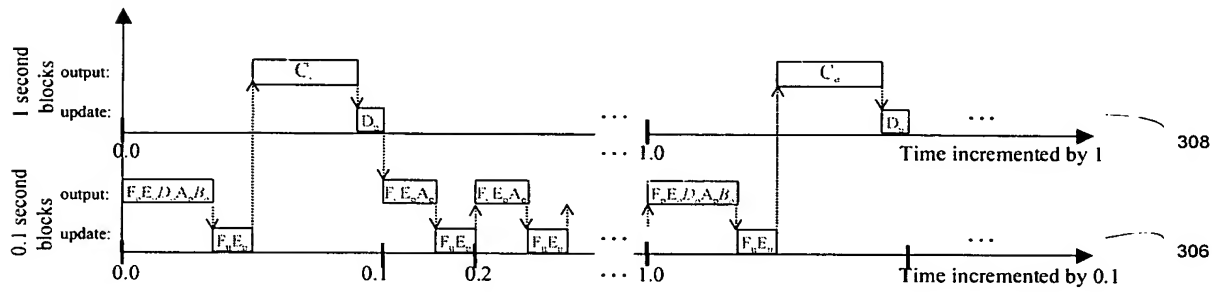


Figure 13

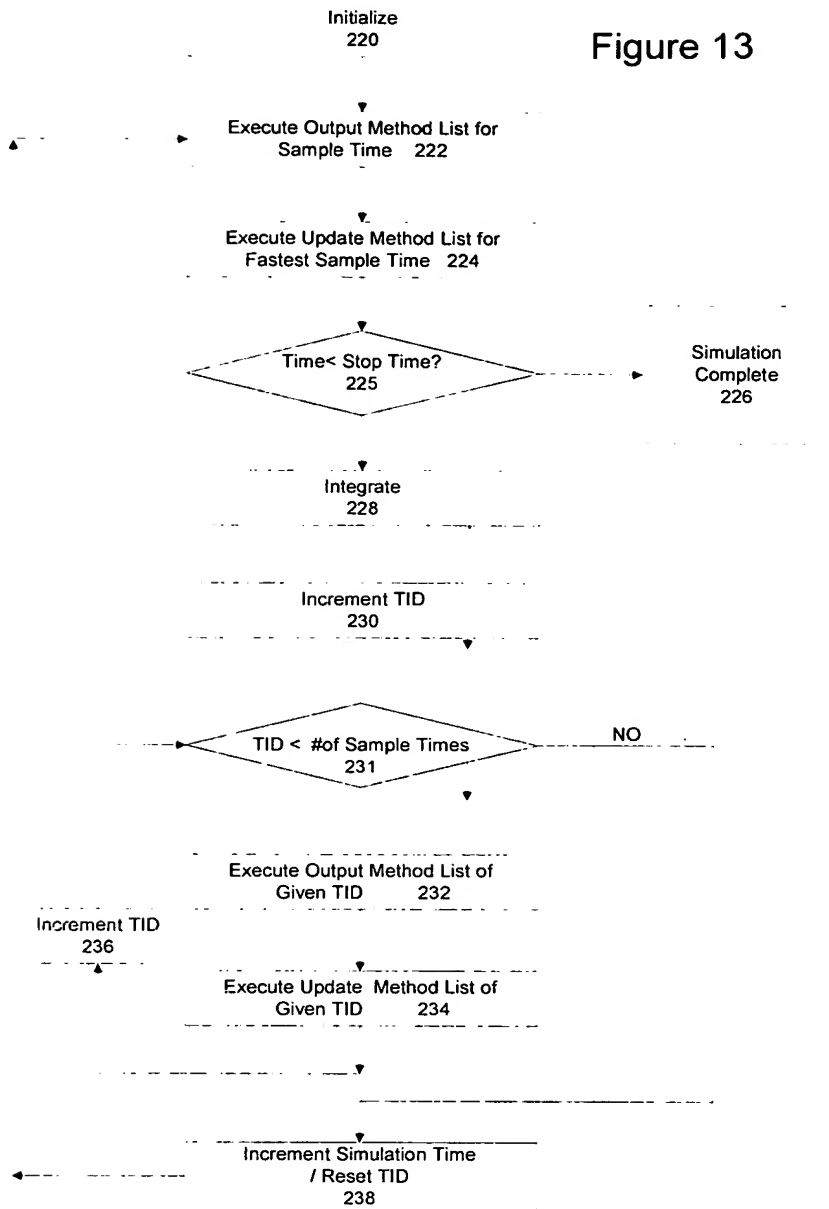
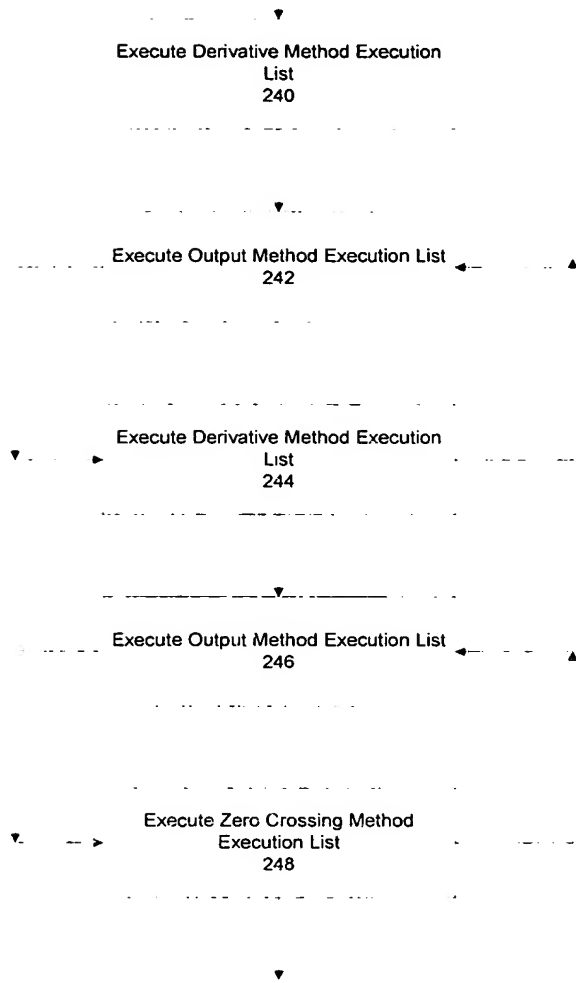


Figure 14



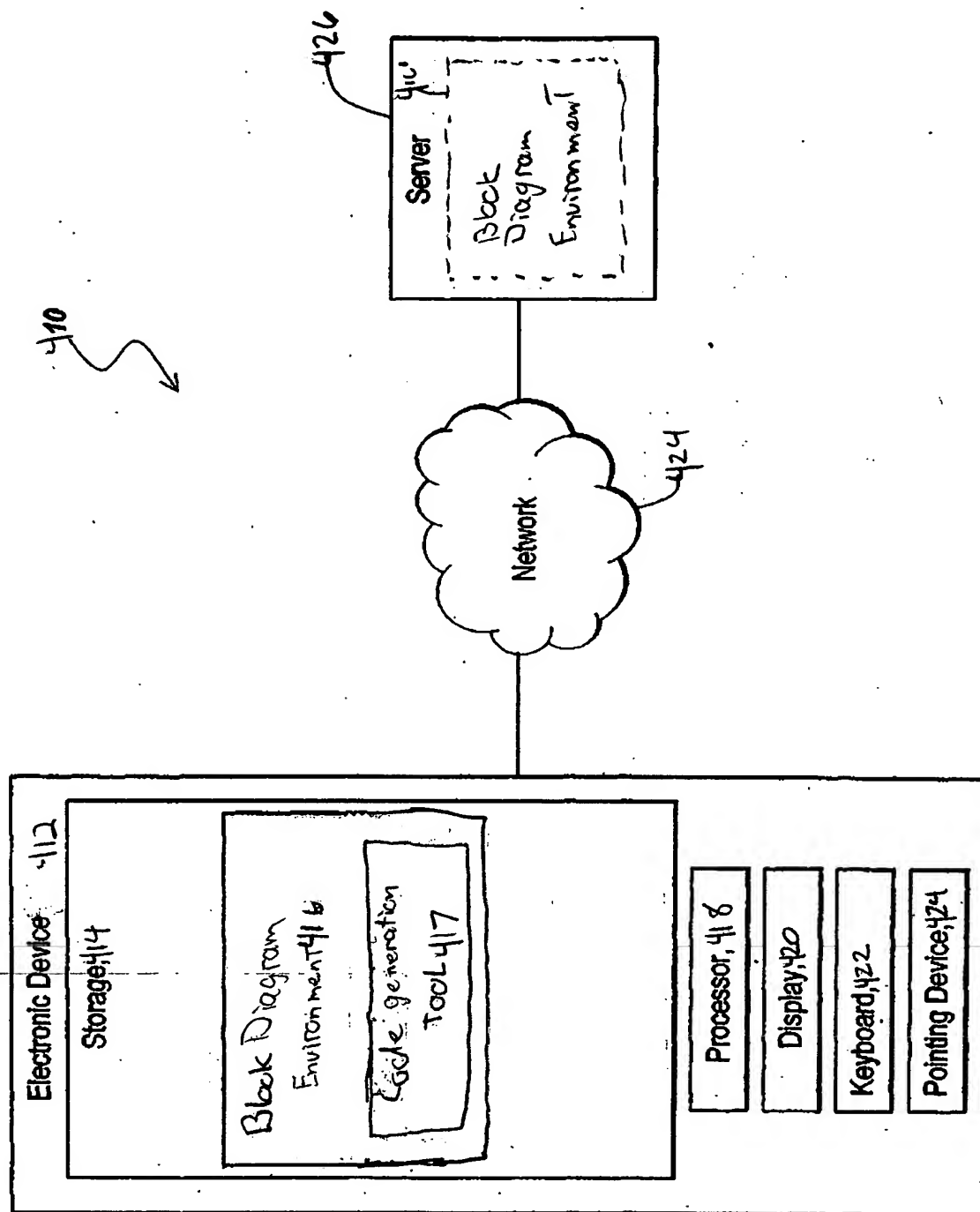
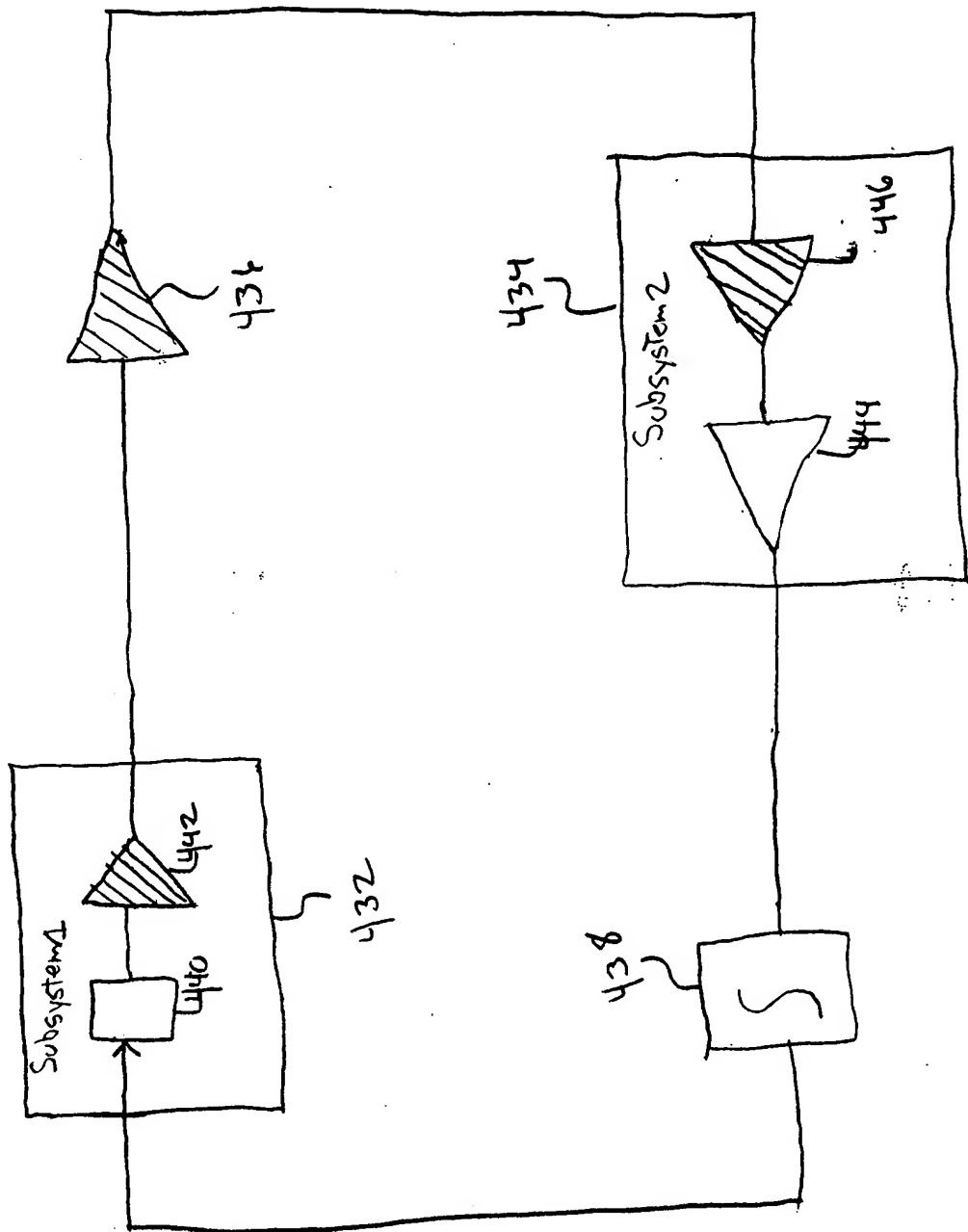


FIGURE 15

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Block Diagram Model



 = First Operating Rate  = Second Operating Rate

FIGURE 16

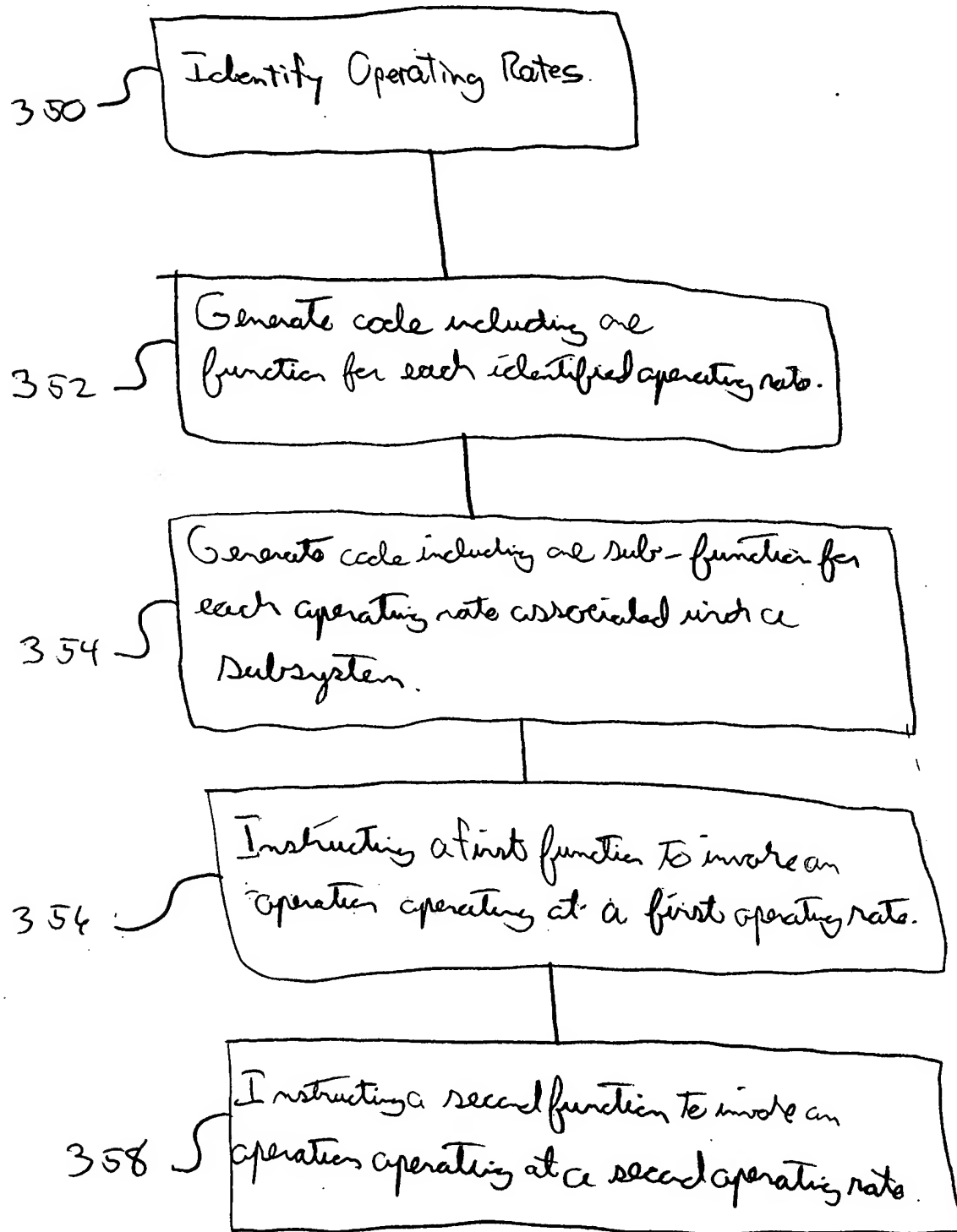
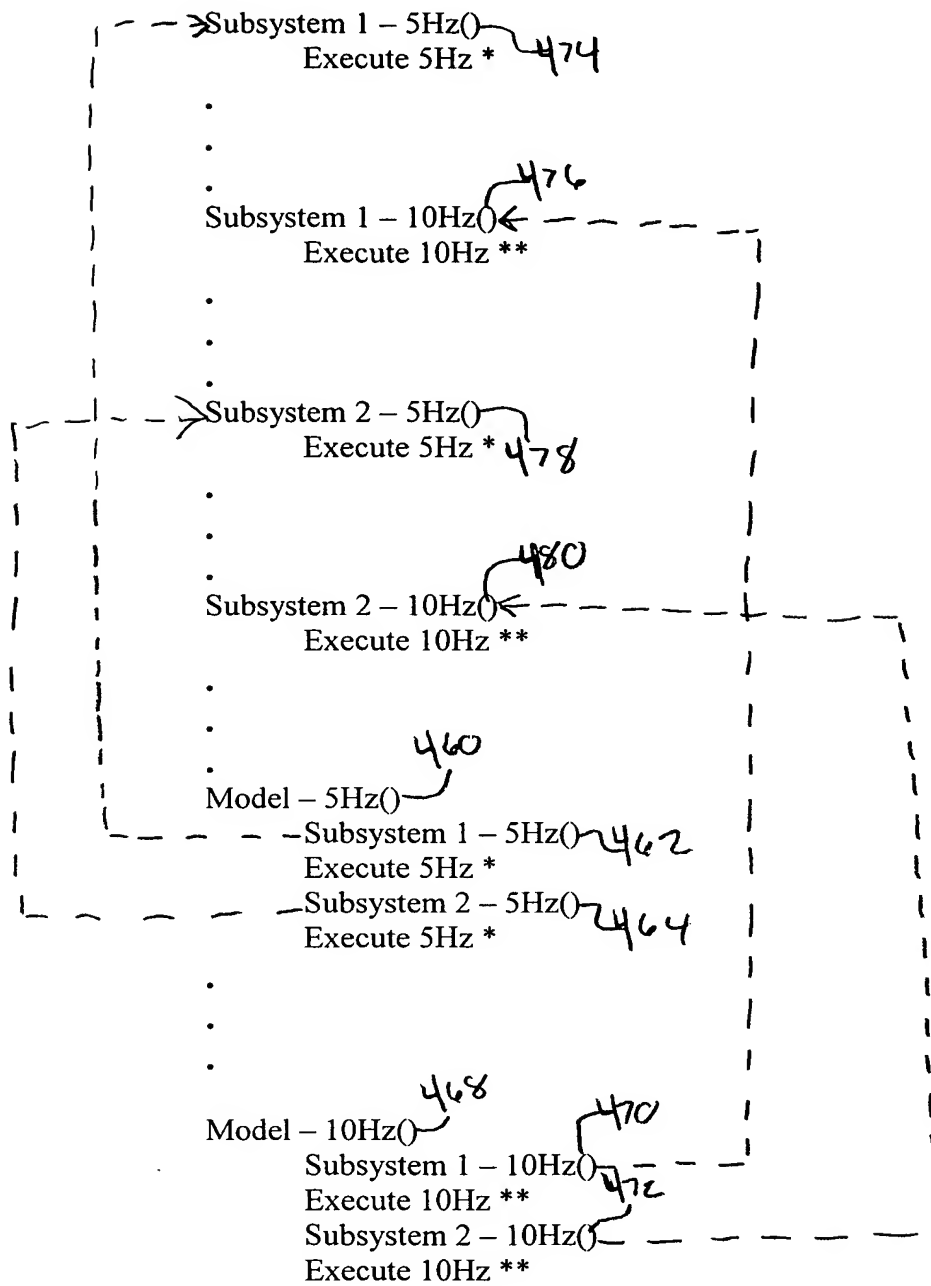


FIGURE 17



* blocks or portions of blocks associated with 5Hz rate

** blocks or portions of blocks associated with 10Hz rate

Figure 18